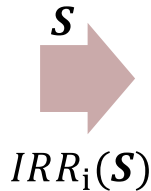


WTs layout
optimization
considering only
maximization of
the AEP

Task 1



Design of the
cable layout using
global optimization
MILP model

Task 2



Feasible design

WTs layout +
Cable layout

$IRR_o(S)$